	Application No.	Applicant(s)
Notice of Allowability	10/652,804	MATSUMOTO, SHOICHIRO
	Examiner	Art Unit
	Tammy Pham	2675
The MAILING DATE of this communication appears on the cover sheet with the correspondence address—All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. 1. This communication is responsive to 4/27/2006.		
2. The allowed claim(s) is/are <u>1-12</u> .		
 3.		
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 4-2+00 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. Interview Summan Paper No./Mail Da 7. Examiner's Amend 8. Examiner's Statem 9. Other	ate / _ /

U.S. Patent and Trademark Office PTOL-37 (Rev. 7-05)

DETAILED ACTION

Allowable Subject Matter

Claims 1-12 are allowed.

The following is a statement of reasons for the indication of allowable subject matter:

Claims 1-5 are allowable because prior art fails to teach or suggest, either alone or in combination, a system and method for driving a display comprising:

- A voltage converter transistor having one terminal connected to the other terminal
 of the selection transistor, the other terminal connected to a power supply, and a
 gate connected to the gate of the driver transistor.
- A data line driven with a current corresponding to data and the gate line is driven
 so that the selection transistor and the short-circuiting transistor are switched on
 and the current corresponding to the data flows through the voltage converter
 transistor to charge the capacitor to a voltage correspond to the data.

Claims 6-12 are allowable because prior art fails to teach or suggest, either alone or in combination, a system and method for driving a display comprising:

- A voltage converter transistor having one terminal connected to the other terminal
 of the selection transistor, the other terminal connected to a power supply, and a
 gate connected to the gate of the driver transistor.
- A data line driven with a current corresponding to data and the gate line is driven so that the selection transistor and the short-circuiting transistor are switched on by a write timing signal.

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Machio et al. (Japanese Publication No. 2001-147659) discloses a circuit in which two pchannel TFTs are added in each pixel as controller transistors and a signal current is applied to a data line. However, Machio is lacking a voltage converter transistor where one terminal is connected to the other terminal of the selection transistor, the other terminal connected to a power supply, and a gate connected to the gate of the driver transistor.

Shimizu et al. (Japanese Patent No: 59178961 A) discloses a voltage converter. However, Shimizu does not disclose this component as specified in claim 1.

Matsumoto et al. (US Patent No: 6,954,194) discloses of a semiconductor device for controlling an element to be driven which includes a switching TFT and various similar components to the application. However, Matsumoto still lacks the exact make up for the circuit as describe in claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tammy Pham whose telephone number is (571) 272-7773. The examiner can normally be reached on 8:00-5:30 (Mon-Fri).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (571) 272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tammy Pham August 15, 2006

> RICHARD HJERPE SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600